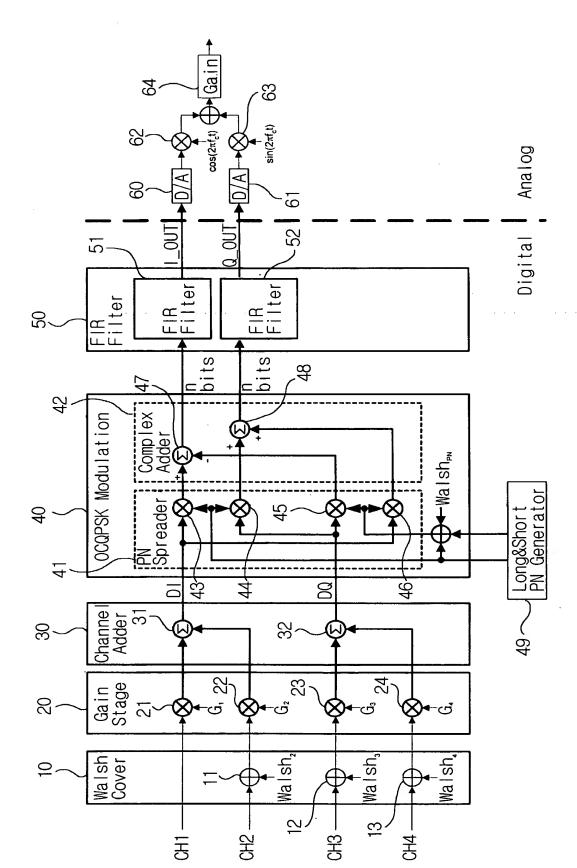
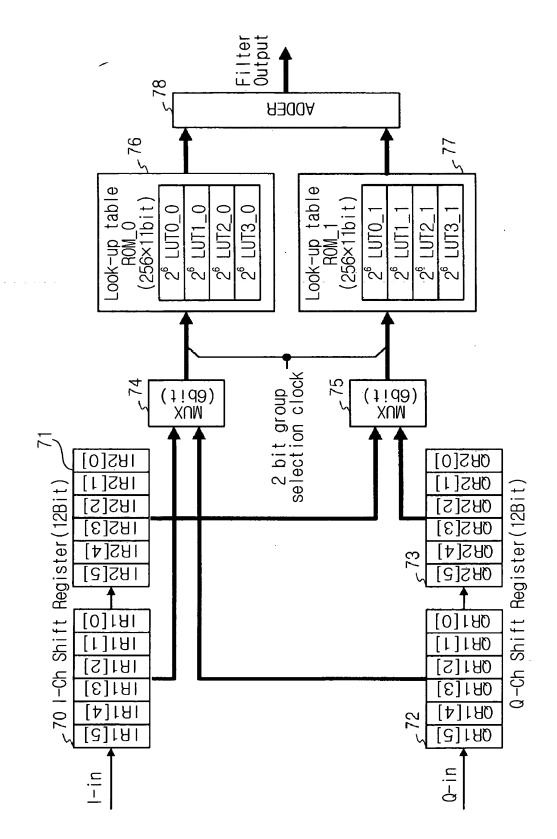
FIG. 1



FJG. 2



Pipeline register I 8 Group selector 800 Pipeline s 90/ 8 20 000 Look-up table group0 Look-up table group3 Look-up table group2 Look-up table 300 group1 Address generator 200 8 F10 F13

F00

<u>면</u>

F02

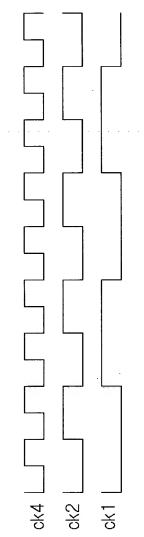
F03

## 

## FIG. 4

Total 108 Tap	0 1 22233	24 25  ···  46 47	48 ··· 59 60	1 22 23 24 25 46 47 48  59 60 61   82 83 84 85   106 107	85 ··· 106107
		-  -  -  -		1	
Look-up table group 0		24 28 32 36 40 44 2 <sup>5</sup> LUTO_1	48 52 56 60   2\LUT0_C	4   8   12   16   20   24   28   32   36   40   44   48   52   56   60   64   68   72   76   80   84   88   92   96   100   104   25   LUTO_3   25   LUTO_	88[92]96[100]104 2 <sup>5</sup> LUT0_3
Look-up table group 1	1 5 9 13 17 21 2 <sup>5</sup> LUT1_0	25[29[33[37]41]45 2 <sup>5</sup> LUT1_1	49 53 57 61 2^LUT1_C	5   9   13   17   21   25   29   33   37   41   45   49   53   57   61   65   66   73   77   81   85   89   93   97   10   11 0   2°   LUT1_3   2°   LUT1_	89 <u>93 97 101 105</u> 2 <sup>5</sup> LUT1_3
Look-up table group2		26 30 34 38 42 46 2 <sup>5</sup> LUT2_1	50 54 58 62  2^LUT2_C	2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62 66 70 74 78 82 86 90 94 98 10 210 6 2	90 94 98 102 106 2 <sup>5</sup> LUT2_3
Look-up table group 3		27 31 35 39 43 47 2 <sup>5</sup> LUT3_1	751555963 2LUT3_C	3 7 11115 19 23 27 31 35 39 43 47 51 55 59 63 67 71 75 79 83 87 9 195 99 103 107 25 LUT 3_0 25 LUT 3_1 2 LUT 3_3	91]95]99]103[107] 2 <sup>5</sup> LUT3_3
Input shift register & selector ouput	6 bit A[26:21]	6 bit 3 bit 6 bit A[20:15] A[14:12] A[11:6]	3 bit A[14:12]	6 bit A[11:6]	6 bit A[5:0]

FIG. 5



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FIG. 6

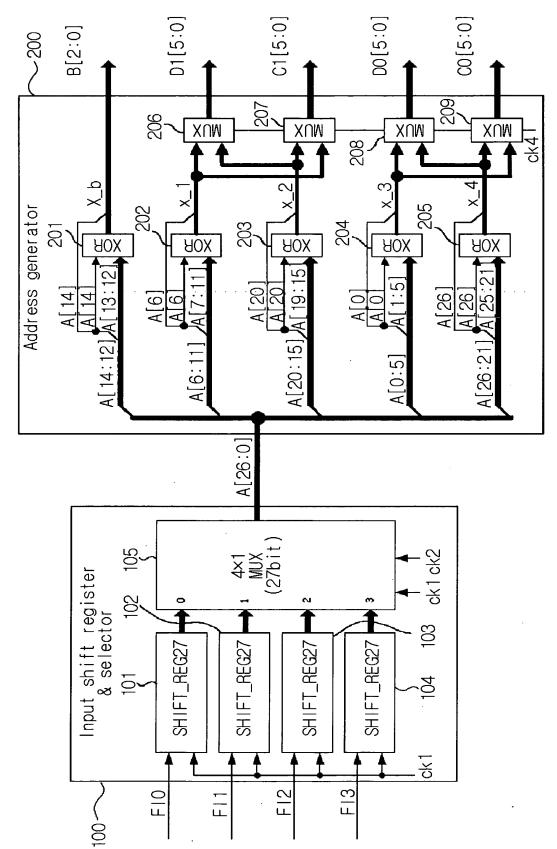


FIG. 7

